

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Yoshimasa NAGAKURA et al.**

Serial Number: **Not Yet Assigned**

Filed: **November 13, 2003**

Customer No.: 38834

For: **METHOD FOR FABRICATING A SEMICONDUCTOR DEVICE**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

November 13, 2003

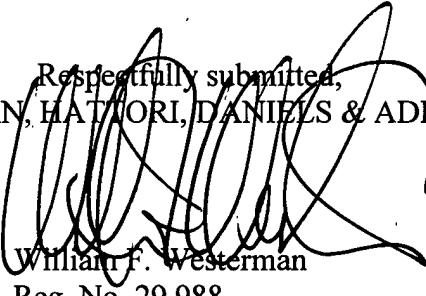
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; References (4)

**INFORMATION
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STATEMENT
PTO-1449**

Atty. Docket No. 032105	Serial No. New Appln.
Applicant(s): Yoshimasa NAGAKURA et al.	
Filing Date: November 13, 2003	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA					
	AB					
	AC					
	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
	AE	6-140572	05/20/94	Japan Abstract.
	AF	7-111253	04/25/95	Japan Abstract.
	AG	2001-244264	09/07/01	Japan Abstract.
	AH	2001-338976	12/07/01	Japan Abstract.
	AI			

OTHER DOCUMENTS

	AJ	
	AK	

Examiner

Date Considered